

VLSI SOCIETY OF INDIA



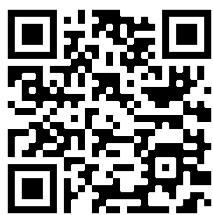
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भारतीय प्रौद्योगिकी
संस्थान जम्मू
INDIAN INSTITUTE OF
TECHNOLOGY JAMMU

26th International Symposium on VLSI Design and Test (VDAT-2022)

Theme: Chips to Startup for Sustainable Development

More Details



17 – 19 July 2022

Design Contest

So, what is your Design/Idea?

DC Submission Deadline
10th June 2022

Invites original designs from academic institutions, R&D labs, and industry to challenge the status quo and present their most exciting design concepts/prototypes to design experts

- **Embedded Systems/IoT/Electronics**
System/Design/RTL/Algorithm Logic/Circuit/Module/ Architecture
- **How it advances the specific area with a well-defined application**

AI for Healthcare
 Cyber-Physical Systems
 Neuromorphic Computing
 Hardware Security

Advance Computing
 Communication Technologies
 VLSI Design
 Electronics System Design

Takeaway

- **Award with Certificate of appreciation for the top 3 designs**
- **Abstract featured in the hard proceeding of the VDAT-2022**

Download the VDAT DC template upload it on the link given on the conference website

Design Contest Submission Link: <https://tinyurl.com/2p97v29u>

Website: www.iitjammu.ac.in/vdat2022
Email: vdat2022.dc@gmail.com



Conference Chair
Dr. Ambika Prasad Shah
IIT Jammu

Design Contest Co-Chairs
Dr. Kankat Ghosh, IIT Jammu
Dr. Bhupendra S. Reniwal, IIITDM Kancheepuram